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| **IMPORTANT NOTES** |
| 1. **All applied voltages and fault levels specified are inclusive and the actual test value must not be outside the specified limits.** 2. **On power up and before testing begins, wait for RED LED to turn off. This signals the calibration stage is complete.** |

| **Test No.** | **Test Description** | **Applied Voltage** | **Applied**  **Fault Level** | **Test Result** |
| --- | --- | --- | --- | --- |
| 1 | Check Response Light and Output Turns Off | 13.5V | No fault applied | Response Light and Output Turns Off |
| 2 | Measure current consumption at maximum voltage. | 13.5V | No fault applied | Current Consumption  **1mA ≤ Is ≤ 5mA DC** |
| 3 | Apply Lower Level DC I  Increment Fault Current to Upper Level DC I or until Device Trips  *(Fault Current Level to be applied for sufficient time to allow device respond - e.g. 1mA/second*  *LL DC I and UL DC I applied for 1 sec)* | 13.5V | LL DC I - 3.2mA  UL DC I – 5.8mA | Must Not Respond at LL DC I and Must Respond before UL DC I |
| 4 | Measure Trip Time at IΔn DC I | 10.7V | 5.9 – 6mA | Response time must be **≤800ms** |
| 5 | Apply Lower Level DC II  Increment Fault Current to Upper Level DC II or until Device Trips  *(Fault Current Level to be applied for sufficient time to allow device respond - e.g. 1mA/second*  *LL DC II and UL DC II applied for 1 sec)* | 13.5V | LL DC II - 3.2mA  UL DC II – 5.8mA | Must Not Respond at LL DC II and Must Respond before UL DC II |
| 6 | Measure Trip Time at IΔn DC II | 10.7V | 5.9 – 6mA | Response time must be **≤800ms** |
| 7 | Test Winding. Lower Level Vdc applied to test input | 10.7V | No fault applied | Must Respond |
| 8 | Measure Trip Time at 10IΔn DC I | 10.7V | 59 – 60mA | Response time must be **≤130ms** |
| 9 | Measure Trip Time at 10IΔn DC II | 10.7V | 59 – 60mA | Response time must be **≤130ms** |
| 10 | Apply 100mA – 50Hz  Apply fault for at least 1 second | 10.7V | 100 - 105mA | Must Not Respond |

**Document Revision History**

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| --- | --- | --- | --- | --- |
| **Rev** | **Date** | **Description** | **Author** | **Approved By** |
| 1.0 | 14/12/2017 | Initial document | Paul Kelly |  |
| 1.1 | 26/09/2018 | Test Sequence updated for New Tester | Paul Kelly |  |